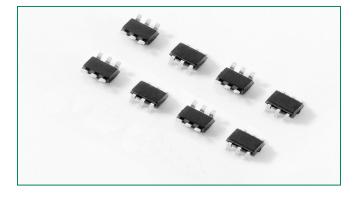


SP0504S Series 0.85pF Diode Array

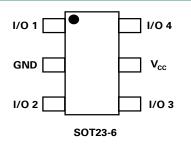
AUTOMOTIVE GRADE RoHS

(P'a)

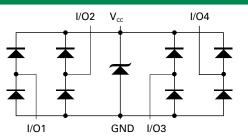
GREEN



Pinout



Functional Block Diagram



Additional Information







Samples

Description

The SP0504S has ultra low capacitance rail-to-rail diodes with an additional zener diode fabricated in a proprietary silicon avalanche technology to protect each I/O pin providing a high level of protection for electronic equipment that may experience destructive electrostatic discharges (ESD). These robust diodes can safely absorb repetitive ESD strikes at the maximum level (Level 4) specified in the IEC 61000-4-2 international standard without performance degradation. Their very low loading capacitance also makes them ideal for protecting high speed signal pins such as HDMI, DVI, USB2.0, and IEEE 1394.

(5/50ns)

Low leakage current of

• Small packaging options

Lightning Protection, IEC

• Network Hardware/Ports

61000-4-5, 4.5A (8/20µs)

0.5µA (MAX) at 5V

saves board space

AEC-Q101 gualified

Test Equipment

Medical Equipment

Automotive Network

Features

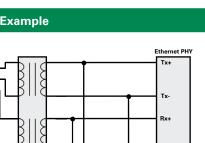
- RoHS compliant and lead-free
- Low capacitance of 0.85 pF (TYP) per I/O
- ESD protection of ±12kV contact discharge, ±15kV air discharge, (IEC 61000-4-2)
- EFT protection, IEC 61000-4-4, 40A

Applications

- Computer Peripherals
- Mobile Phones
- PDA's
- Digital Cameras

R.I-45

Application Example



A single 4 channel SP0504S device can be used to protect four of the data lines in a HDMI/DVI interface. Two (2) SP0504S devices provide protection for the main data lines. Low voltage ASIC HDMI/DVI drivers can also be protected with the SP0504S, the +V_{CC} pins on the SP0504S can be substituted with a suitable bypass capacitor or in some backdrive applications the +V_{CC} of the SP0504S can be floated or NC.

GND

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.



Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
I _{PP}	Peak Current (t _p =8/20µs)	4.5	А		
T _{OP}	Operating Temperature	-40 to 125	°C		
T _{STOR}	Storage Temperature	–55 to 150	°C		

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics (T_{OP}=25°C)

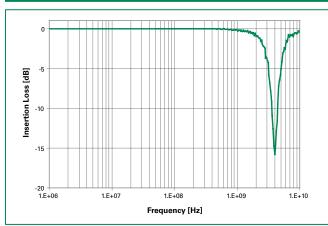
Thermal Information

Parameter	Rating	Units
Storage Temperature Range	–55 to 150	°C
Maximum Junction Temperature	150	°C
Maximum Lead Temperature (Soldering 20-40s)	260	°C

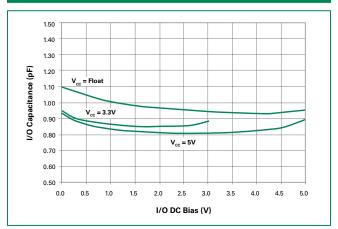
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Standoff Voltage	V _{RWM}	$I_R \le 1\mu A$			6.0	V
Reverse Leakage Current	ILEAK	V _R =5V			0.5	μA
Clamp Voltage ¹	V _c	I _{PP} =1A, t _p =8/20µs, Fwd		9.5	11.0	V
Clamp voltage		I _{PP} =2A, t _p =8/20µs, Fwd		10.6	13.0	V
ESD Withstand Voltage ¹		IEC 61000-4-2 (Contact)	±12			kV
	V _{ESD}	IEC 61000-4-2 (Air)	±15			kV
Diada Canacitanaa1	C _{I/O-GND}	Reverse Bias=0V	0.95	1.1	1.25	pF
Diode Capacitance ¹		Reverse Bias=1.65V	0.7	0.85	1.0	pF
Diode Capacitance ¹	C _{I/O-I/O}	Reverse Bias=0V		0.5		pF

Note: 1. Parameter is guaranteed by design and/or device characterization.

Insertion Loss (S21) I/O to GND

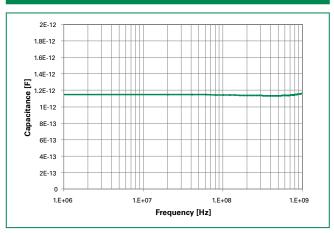


Capacitance vs. Bias Voltage





Capacitance vs. Frequency



Soldering Parameters

Reflow Condition		Pb – Free assembly		
	-Temperature Min (T _{s(min)})	150°C		
Pre Heat	-Temperature Max (T _{s(max)})	200°C		
	-Time (min to max) (t _s)	60 – 180 secs		
Average ra (T _L) to pea	amp up rate (Liquidus) Temp k	3°C/second max		
T _{S(max)} to T _L	- Ramp-up Rate	3°C/second max		
Reflow	-Temperature (T _L) (Liquidus)	217°C		
	-Temperature (t _L)	60 – 150 seconds		
PeakTemperature (T _P)		260+ ^{0/-5} °C		
Time within 5°C of actual peak Temperature (t _p)		20 – 40 seconds		
Ramp-down Rate		6°C/second max		
Time 25°C	to peakTemperature (T _P)	8 minutes Max.		
Do not exc	ceed	260°C		

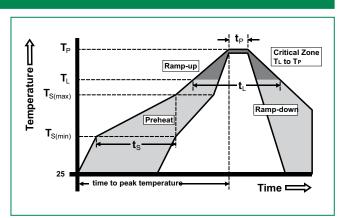
Product Characteristics

Lead Plating	Matte Tin		
Lead Material	Copper Alloy		
Lead Coplanarity	0.0004 inches (0.102mm)		
Substitute Material	Silicon		
Body Material	Molded Epoxy		
Flammability	UL 94 V-0		

Notes :

1. All dimensions are in millimeters 2. Dimensions include solder plating.

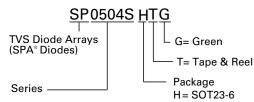
Dimensions are exclusive of mold flash & metal burr.
Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
Package surface matte finish VDI 11-13.



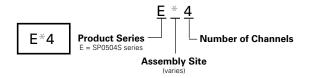
Ordering Information

Part Number	Package	Marking	Min. Order Qty.
SP0504SHTG	SOT23-6	E*4	3000

Part Numbering System

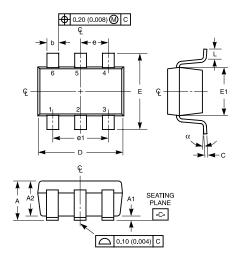


Part Marking System



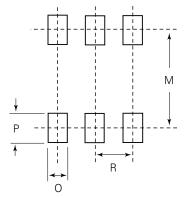


Package Dimensions - SOT23-6

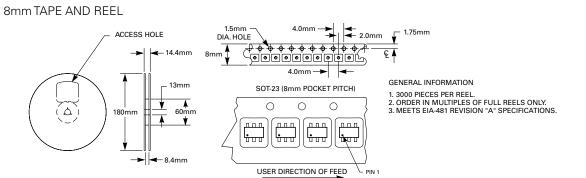


Package	SOT23					
Pins	6					
JEDEC	MO-178AB					
	Millimeters		Inches		Netes	
	Min	Max	Min	Max	Notes	
Α	0.900	1.450	0.035	0.057	-	
A1	0.000	0.150	0.000	0.006	-	
A2	0.900	1.300	0.035	0.051	-	
b	0.350	0.500	0.0138	0.0196	-	
С	0.080	0.220	0.0031	0.009	-	
D	2.800	3.000	0.11	0.118	3	
E	2.600	3.000	0.102	0.118	-	
E1	1.500	1.750	0.06	0.069	3	
е	0.95 Ref		0.0374 ref		-	
e1	1.9 Ref		0.0748 Ref		-	
L	0.30	0.600	0.012	0.023	4,5	
N	6		6		6	
α	0°	8°	0°	8°	-	
М		2.590		0.102	-	
0		0.690		.027 TYP	-	
Р		0.990		.039 TYP	-	
R		0.950		0.038	-	

Recommended Solder Pad Layout



Embossed Carrier Tape & Reel Specification - SOT23-6



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Notes:

1. 2.

Dimensioning and tolerancing Per ASME Y14.5M-1994. Package conforms to EIAJ SC-74 (1992). Dimensions D and E1 are exclusive of mold flash, protrusions, or gate burrs. 3.

4

Foot length L measured at reference to seating plane. "L" is the length of flat foot surface for soldering to substrate. "N" is the number of terminal positions. 5.

6.

7. Controlling dimension: MILLIMETER. Converted inch dimensions are not necessarily exact.