ROHS COMPLIANT



Vishay General Semiconductor

# Surface Mount Trench MOS Barrier Schottky Rectifier



DO-214AA (SMB)

PRIMARY CHARACTERISTICS			
Package	DO-214AA (SMB)		
I <sub>F(AV)</sub>	4.0 A		
V <sub>RRM</sub>	100 V		
I <sub>FSM</sub>	80 A		
E <sub>AS</sub>	50 mJ		
$V_F$ at $I_F = 4.0$ A	0.61 V		
T <sub>J</sub> max.	150 °C		
Diode variations	Single die		

### FEATURES

- Low profile package
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

## **MECHANICAL DATA**

**Case:** DO-214AA (SMB) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VSSB410S	UNIT		
Device marking code		V4B			
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V		
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	4.0			
	I <sub>F</sub> <sup>(2)</sup>	1.9	A		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	80	А		
Non-repetitive avalanche energy at $T_J = 25 \text{ °C}$ , L = 60 mH	E <sub>AS</sub>	50	mJ		
Peak repetitive reverse current at $t_p$ = 2 µs, 1 kHz, $T_J$ = 38 °C ± 2 °C	I <sub>RRM</sub>	1.0	А		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C		

Notes

<sup>(1)</sup> Mounted on 14 mm x 14 mm pad areas, 1 oz. FR4 PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

Document Number: 89140

VSSB410S



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	V <sub>BR</sub>	100 (minimum)	-	V
Instantaneous forward voltage	I <sub>F</sub> = 4.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.68	0.77	V
		T <sub>A</sub> = 125 °C		0.61	0.69	
Reverse current	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	1.5	-	μA
		T <sub>A</sub> = 125 °C		1.2	-	mA
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		7.0	250	μA
		T <sub>A</sub> = 125 °C		3.6	20	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	230	-	pF

Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL VSSB410S		UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	120	°C/W	
	R <sub>0JM</sub> <sup>(2)</sup>	15	0/10	

#### Notes

 $^{(1)}$  Free air, mounted on recommended PCB 1 oz. pad area. Thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(2)}$  Units mounted on PCB with 14 mm x 14 mm copper pad areas.  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSB410S-E3/52T	0.096	52T	750	7" diameter plastic tape and reel	
VSSB410S-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)



Fig. 1 - Maximum Forward Current Derating Curve



Fig. 2 - Forward Power Loss Characteristics

Revision: 05-Nov-13

2

Document Number: 89140

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Fig. 3 - Typical Instantaneous Forward Characteristics



Fig. 4 - Typical Reverse Characteristics



Fig. 5 - Typical Junction Capacitance



Fig. 6 - Typical Transient Thermal Impedance

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



#### **Mounting Pad Layout**



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