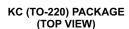


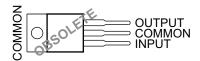
FIXED POSITIVE VOLTAGE REGULATORS

Check for Samples: µA7800 SERIES

FEATURES

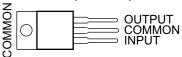
- 3-Terminal Regulators
- Available in fixed 5V/8V/10V/12V/15V/24V options
- Output Current up to 1.5 A



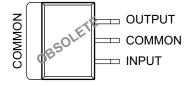


- Internal Thermal-Overload Protection
- High Power-Dissipation Capability
- Internal Short-Circuit Current Limiting
- Output Transistor Safe-Area Compensation

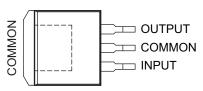




KTE (PowerFLEX™) PACKAGE (TOP VIEW)



KTT (TO-263) PACKAGE (TOP VIEW)



DESCRIPTION/ORDERING INFORMATION

This series of fixed-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. Each of these regulators can deliver up to 1.5 A of output current. The internal current-limiting and thermal-shutdown features of these regulators essentially make them immune to overload. In addition to use as fixed-voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents, and also can be used as the power-pass element in precision regulators.

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



ORDERING INFORMATION(1)

TJ	V _{O(NOM)}	PACKAGE ⁽²)	ORDERABLE PART NUMBER	TOP-SIDE MARKING
		TO-220, short shoulder – KCS	Tube of 50	UA7805CKCS	UA7805C
		TO-220, single gauge – KCT	Tube of 50	UA7805CKCT	UA7805C
	5 V	TO-263 – KTT	Reel of 500	UA7805CKTTR	UA7805C
		PowerFLEX™ – KTE		OBSOLETE	OBSOLETE
		TO-220 – KC		OBSOLETE	OBSOLETE
		TO-220, short shoulder – KCS	Tube of 50	UA7808CKCS	UA7808C
		TO-220, single gauge – KCT	Tube of 50	UA7808CKCT	UA7808C
	8 V	TO-263 – KTT	Reel of 500	UA7808CKTTR	UA7808C
		PowerFLEX – KTE		OBSOLETE	OBSOLETE
		TO-220 – KC		OBSOLETE	OBSOLETE
		TO-220, short shoulder – KCS	Tube of 50	UA7810CKCS	UA7810C
	40.1/	TO-263 – KTT	Reel of 500	UA7810CKTTR	UA7810C
	10 V	PowerFLEX – KTE		OBSOLETE	UA7808C UA7808C UA7808C UA7808C OBSOLETE OBSOLETE UA7810C UA7810C OBSOLETE OBSOLETE UA7812C
00.1- 40500		TO-220 – KC		OBSOLETE	OBSOLETE
°C to 125°C		TO-220, short shoulder – KCS	Tube of 50	UA7812CKCS	UA7812C
		TO-220, single gauge – KCT	Tube of 50	UA7812CKCT	UA7812C
	12 V	TO-263 – KTT	Reel of 500	UA7812CKTTR	UA7812C
		PowerFLEX – KTE		OBSOLETE	OBSOLETE
		TO-220 – KC		OBSOLETE	OBSOLETE
		TO-220, short shoulder – KCS	Tube of 50	UA7815CKCS	UA7815C
		TO-220, single gauge – KCT	Tube of 50	UA7815CKCT	UA7815C
	15 V	TO-263 – KTT	Reel of 500	UA7815CKTTR	UA7815C
		PowerFLEX – KTE		OBSOLETE	OBSOLETE
		TO-220 – KC		OBSOLETE	MARKING UA7805C UA7805C UA7805C OBSOLETE OBSOLETE UA7808C UA7808C UA7808C UA7808C UA7810C UA7810C UA7810C UA7812C UA7812C UA7812C UA7812C UA7812C UA7812C UA7812C UA7815C UA7815C
		TO-220, short shoulder – KCS	Tube of 50	UA7824CKCS	UA7824C
	24.1/	TO-263 – KTT	Reel of 500	UA7824CKTTR	UA7824C
	24 V	PowerFLEX – KTE		OBSOLETE	OBSOLETE
		TO-220 – KC		OBSOLETE	OBSOLETE

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



Figure 1. SCHEMATIC **INPUT** OUTPUT COMMON

Absolute Maximum Ratings(1)

over virtual junction temperature range (unless otherwise noted)

			MIN	MAX	UNIT
.,	lonut voltogo	μA7824C		40	V
VI	Input voltage	All others		35	V
T_{J}	Operating virtual junction temperature			150	°C
	Lead temperature	1,6 mm (1/16 in) from case for 10 s		260	°C
T _{stg}	Storage temperature range		-65	150	°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Package Thermal Data⁽¹⁾

PACKAGE	BOARD	θ_{JA}	θ _{JC}	θ _{JP} ⁽²⁾
PowerFLEX (KTE) – OBSOLETE	High K, JESD 51-5	23°C/W	3°C/W	2.7°C/W
TO-220 (KCS), (KCT) (KC – OBSOLETE)	High K, JESD 51-5	19°C/W	17°C/W	3°C/W
TO-263 (KTT)	High K, JESD 51-5	25.3°C/W	18°C/W	1.94°C/W

Maximum power dissipation is a function of $T_{J(max)}$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_{J(max)} - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability. For packages with exposed thermal pads, such as QFN, PowerPADTM, or PowerFLEX, θ_{JP} is defined as the thermal resistance between the die junction and the bottom of the exposed pad.



Recommended Operating Conditions

			MIN	MAX	UNIT
		μΑ7805	7	25	
		μΑ7808	10.5	25	
V _I Input voltage	μA7810	12.5	28		
	input voitage	μΑ7812	14.5	30	V
		μΑ7815	17.5	30	
		μΑ7824	27	38	
Io	Output current			1.5	Α
T_{J}	Operating virtual junction temperature		0	125	°C



uA7805 Electrical Characteristics

at specified virtual junction temperature, V_I = 10 V, I_O = 500 mA (unless otherwise noted)

DADAMETED	TEST CONDITIONS	T _J ⁽¹⁾	μ	A7805C		LINUT	
PARAMETER	TEST CONDITIONS	I J \''	MIN	TYP	MAX	UNIT	
Output voltage	$I_{O} = 5 \text{ mA to 1 A}, V_{I} = 7 \text{ V to 20 V},$	25°C	4.8	5	5.2	V	
Output voltage	P _D ≤ 15 W	0°C to 125°C	4.75		5.25	V	
land delta a manufation	V _I = 7 V to 25 V	2500		3	100	\/	
Input voltage regulation	V _I = 8 V to 12 V	25°C		1	50	mV	
Ripple rejection ⁽²⁾	V _I = 8 V to 12 V, f = 120 Hz	0°C to 125°C	62	78		٩D	
Ripple rejection V	V _I = 8 V to 12 V, f = 120 Hz (KCT)	0.0 10 125.0		68		dB	
Output voltage regulation	I _O = 5 mA to 1.5 A	25%C		15	100	m\/	
	I _O = 250 mA to 750 mA	25°C		5	5 50	mV	
Output resistance	f = 1 kHz	0°C to 125°C		0.017		Ω	
Temperature coefficient of output voltage	I _O = 5 mA	0°C to 125°C		-1.1		mV/°C	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		40		μV	
Dropout voltage	I _O = 1 A	25°C		2		V	
Bias current		25°C		4.2	8	mA	
Diag gurrant change	V _I = 7 V to 25 V	0°C to 105°C			1.3	A	
Bias current change	I _O = 5 mA to 1 A	0°C to 125°C			0.5	mA	
Short-circuit output current		25°C		750		mA	
Peak output current		25°C		2.2		Α	

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

uA7808 Electrical Characteristics

at specified virtual junction temperature, $V_I = 14 \text{ V}$, $I_O = 500 \text{ mA}$ (unless otherwise noted)

DADAMETER	TEST COMPITIONS	T,, (1)	μ	A7808C		UNIT
PARAMETER	TEST CONDITIONS	1, \''	MIN	TYP	MAX	UNII
Outrast valta as	$I_0 = 5 \text{ mA to 1 A}, V_1 = 10.5 \text{ V to 23 V},$	25°C	7.7	8	8.3	V
Output voltage	P _D ≤ 15 W	0°C to 125°C	7.6		8.4	V
land valence results a	V _I = 10.5 V to 25 V	2500		6	160	\/
Input voltage regulation	V _I = 11 V to 17 V	25°C		2	80	mV
	V _I = 11.5 V to 21.5 V, f = 120 Hz		55	72		
Ripple rejection (2)	V _I = 11.5 V to 21.5 V, f = 120 Hz (KCT)	0°C to 125°C 62		dB		
Outrout walks are no midetion	I _O = 5 mA to 1.5 A	25°C	NE 9 C		160	mV
Output voltage regulation	I _O = 250 mA to 750 mA	25°C		4	80	mv
Output resistance	f = 1 kHz	0°C to 125°C		0.016		Ω
Temperature coefficient of output voltage	$I_O = 5 \text{ mA}$	0°C to 125°C		-0.8		mV/°C
Output noise voltage	f = 10 Hz to 100 kHz	25°C		52		μV
Dropout voltage	I _O = 1 A	25°C		2		V
Bias current		25°C		4.3	8	mA
Dies sument about	V _I = 10.5 V to 25 V	000 +- 40500			1	A
Bias current change	I _O = 5 mA to 1 A	0°C to 125°C			0.5	mA
Short-circuit output current		25°C		450		mA

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

⁽²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.

²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.



uA7808 Electrical Characteristics (continued)

at specified virtual junction temperature, $V_1 = 14 \text{ V}$, $I_0 = 500 \text{ mA}$ (unless otherwise noted)

DADAMETED	PARAMETER TEST CONDITIONS T ₁ ⁽¹⁾		μΑ7808C			UNIT
PARAMETER	TEST CONDITIONS	IJ 、 ′	MIN	TYP	MAX	UNII
Peak output current		25°C		2.2		Α

uA7810 Electrical Characteristics

at specified virtual junction temperature, V_I = 17 V, I_O = 500 mA (unless otherwise noted)

DADAMETED	TEST COMPLETIONS	- (1)	μ	A7810C			
PARAMETER	TEST CONDITIONS	T _J ⁽¹⁾	MIN	TYP	MAX	UNIT	
Outrot valta as	$I_0 = 5 \text{ mA to } 1 \text{ A}, V_1 = 12.5 \text{ V to } 25 \text{ V},$	25°C	9.6	10	10.4	V	
Output voltage	P _D ≤ 15 W	0°C to 125°C	9.5		10.5	V	
Innut valtage regulation	V _I = 12.5 V to 28 V	25°C		7	200	m\/	
Input voltage regulation	V _I = 14 V to 20 V	25 0		2	100	mV	
Ripple rejection (2)	V _I = 13 V to 23 V, f = 120 Hz	0°C to 125°C	55	71		dB	
Output voltage regulation	I _O = 5 mA to 1.5 A	0500		12	200	>/	
	I _O = 250 mA to 750 mA	25°C		4	100	mV	
Output resistance	f = 1 kHz	0°C to 125°C		0.018		Ω	
Temperature coefficient of output voltage	I _O = 5 mA	0°C to 125°C		-1		mV/°C	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		70		μV	
Dropout voltage	I _O = 1 A	25°C		2		V	
Bias current		25°C		4.3	8	mA	
Diag assessed all agents	V _I = 12.5 V to 28 V	000 + 40500			1		
Bias current change	I _O = 5 mA to 1 A	0°C to 125°C			0.5	mA	
Short-circuit output current		25°C		400		mA	
Peak output current		25°C		2.2		Α	

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

uA7812 Electrical Characteristics

at specified virtual junction temperature, $V_I = 19 \text{ V}$, $I_O = 500 \text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T,, (1)	μ	A7812C		UNIT	
PARAMETER	TEST CONDITIONS	IJ ` ′	MIN TYP		MAX	ONIT	
Output voltage	$I_0 = 5 \text{ mA to 1 A}, V_1 = 14.5 \text{ V to 27 V},$	25°C	11.5	12	12.5	V	
Output voltage	P _D ≤ 15 W	0°C to 125°C	11.4		12.6	V	
lanut voltage regulation	V _I = 14.5 V to 30 V	25°C		10	240	mV	
Input voltage regulation	V _I = 16 V to 22 V	25°C		3	120	IIIV	
Ripple rejection (2)	V _I = 15 V to 25 V, f = 120 Hz	0°C to 125°C	55	71		dB	
	V _I = 15 V to 25 V, f = 120 Hz (KCT)	0.0 10 125.0	to 125°C 61			ив	
Output valtage regulation	I _O = 5 mA to 1.5 A	25°C		12	240	mV	
Output voltage regulation	I _O = 250 mA to 750 mA	25°C		4	120	IIIV	
Output resistance	f = 1 kHz	0°C to 125°C		0.018		Ω	
Temperature coefficient of output voltage	$I_O = 5 \text{ mA}$	0°C to 125°C		-1		mV/°C	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		75		μV	
Dropout voltage	I _O = 1 A	25°C		2		V	

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-µF capacitor across the input and a 0.1-µF capacitor across the output

⁽²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.

²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.



uA7812 Electrical Characteristics (continued)

at specified virtual junction temperature, $V_1 = 19 \text{ V}$, $I_0 = 500 \text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T _J ⁽¹⁾	μ	μΑ7812C		UNIT
PARAMETER	TEST CONDITIONS	1, (*)	MIN	TYP	MAX	UNIT
Bias current		25°C		4.3	8	mA
Di I	V _I = 14.5 V to 30 V	000 +- 40500			1	mA
Bias current change	I _O = 5 mA to 1 A	0°C to 125°C			0.5	
Short-circuit output current		25°C		350		mA
Peak output current		25°C		2.2		Α

uA7815 Electrical Characteristics

at specified virtual junction temperature, V_I = 23 V, I_O = 500 mA (unless otherwise noted)

DADAMETED	TEST COMPITIONS	T _{.J} (1)	μ	A7815C		UNIT
PARAMETER	TEST CONDITIONS	1,117	MIN	TYP	MAX	ONIT
Output valta as	$I_O = 5 \text{ mA to 1 A}, V_I = 17.5 \text{ V to 30 V},$	25°C	14.4	15	15.6	V
Output voltage	P _D ≤ 15 W	0°C to 125°C	14.25		15.75	V
Innut valtage regulation	V _I = 17.5 V to 30 V	25%C		11	300	\/
Input voltage regulation	V _I = 20 V to 26 V	25°C		3	150	mV
	V _I = 18.5 V to 28.5 V, f = 120 Hz	0°C to 125°C 54 70 60				
Ripple rejection (2)	V _I = 18.5 V to 28.5 V, f = 120 Hz (KCT)			60		dB
Outrout valte as a seculation	I _O = 5 mA to 1.5 A	25°C		12	300	mV
Output voltage regulation	I _O = 250 mA to 750 mA	25°C		4 150	150	
Output resistance	f = 1 kHz	0°C to 125°C		0.019		Ω
Temperature coefficient of output voltage	I _O = 5 mA	0°C to 125°C		-1		mV/°C
Output noise voltage	f = 10 Hz to 100 kHz	25°C		90		μV
Dropout voltage	I _O = 1 A	25°C		2		V
Bias current		25°C		4.4	8	mA
Disc surrent shares	V _I = 17.5 V to 30 V	000 +- 40500			1	A
Bias current change	I _O = 5 mA to 1 A	0°C to 125°C			0.5	mA
Short-circuit output current		25°C		230		mA
Peak output current		25°C		2.1		Α

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

uA7824 Electrical Characteristics

at specified virtual junction temperature, V_I = 33 V, I_O = 500 mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T. ⁽¹⁾	μ	μA7824C		
PARAMETER	TEST CONDITIONS	IJ 、 ′	MIN	TYP	MAX	UNIT
Output voltage	$I_O = 5 \text{ mA to } 1 \text{ A}, V_I = 27 \text{ V to } 38 \text{ V},$	25°C	23	24	25	\/
	P _D ≤ 15 W	0°C to 125°C	22.8		25.2	. V
Input voltage regulation	V _I = 27 V to 38 V	0500		18	480	mV
Input voltage regulation	V _I = 30 V to 36 V	25°C		6	240	
Ripple rejection (2)	V _I = 28 V to 38 V, f = 120 Hz	0°C to 125°C	50	66		dB

⁽¹⁾ Pulse-testing techniques maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output.

⁽²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.

⁽²⁾ This parameter is validated by design and verified during product characterization. It is not tested in production.



uA7824 Electrical Characteristics (continued)

at specified virtual junction temperature, V_{I} = 33 V, I_{O} = 500 mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T _J ⁽¹⁾	μΑ7824C			LINUT
		1, 1,	MIN	TYP	MAX	UNIT
Output voltage regulation	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$	25°C		12	480	mV
	I _O = 250 mA to 750 mA	25 C		4	240	
Output resistance	f = 1 kHz	0°C to 125°C		0.028		Ω
Temperature coefficient of output voltage	$I_O = 5 \text{ mA}$	0°C to 125°C		-1.5		mV/°C
Output noise voltage	f = 10 Hz to 100 kHz	25°C		170		μV
Dropout voltage	I _O = 1 A	25°C		2		V
Bias current		25°C		4.6	8	mA
Bias current change	V _I = 27 V to 38 V	0°C to 125°C			1	mA
	$I_O = 5 \text{ mA to } 1 \text{ A}$	0 0 125 0			0.5	
Short-circuit output current		25°C		150		mA
Peak output current		25°C		2.1		Α

APPLICATION INFORMATION

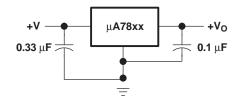


Figure 2. Fixed-Output Regulator

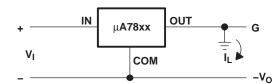
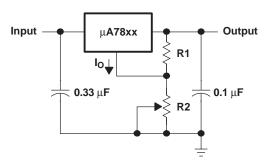


Figure 3. Positive Regulator in Negative Configuration (V_I Must Float)



A: The following formula is used when V_{XX} is the nominal output voltage (output to common) of the fixed regulator:

$$V_{O} = V_{xx} + \left(\frac{V_{xx}}{R1} + I_{Q}\right)R2$$

Figure 4. Adjustable-Output Regulator



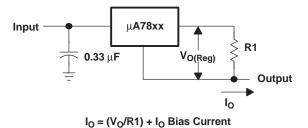


Figure 5. Current Regulator

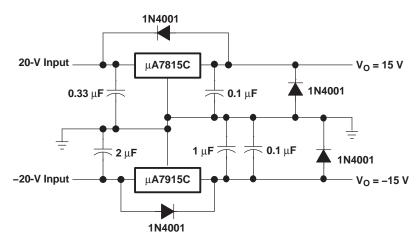


Figure 6. Regulated Dual Supply

Operation With a Load Common to a Voltage of Opposite Polarity

In many cases, a regulator powers a load that is not connected to ground but, instead, is connected to a voltage source of opposite polarity (e.g., operational amplifiers, level-shifting circuits, etc.). In these cases, a clamp diode should be connected to the regulator output as shown in Figure 7. This protects the regulator from output polarity reversals during startup and short-circuit operation.

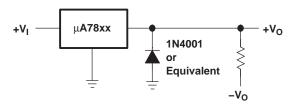


Figure 7. Output Polarity-Reversal-Protection Circuit

Reverse-Bias Protection

Occasionally, the input voltage to the regulator can collapse faster than the output voltage. This can occur, for example, when the input supply is crowbarred during an output overvoltage condition. If the output voltage is greater than approximately 7 V, the emitter-base junction of the series-pass element (internal or external) could break down and be damaged. To prevent this, a diode shunt can be used as shown in Figure 8.



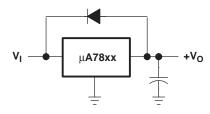


Figure 8. Reverse-Bias-Protection Circuit





REVISION HISTORY

Changes from Revision M (January 2009) to Revision N		
Added KCT package and orderable part number to the ORDERING INFORMATION tab	e 2	
Changes from Revision N (June 2012) to Revision O	Page	
Added KCT Orderable Part Numbers for 8V & 12V	2	

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